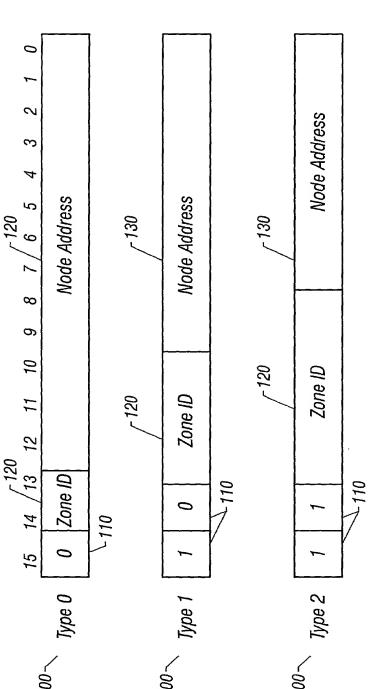
Title:

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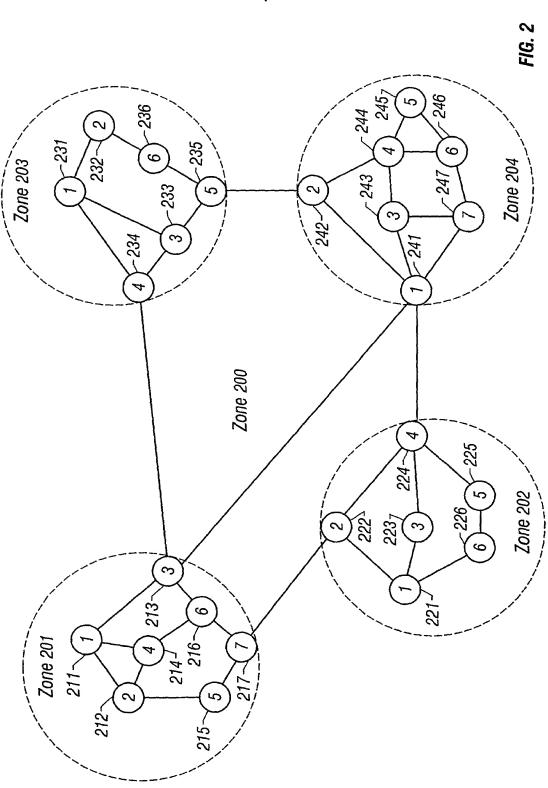


16. 1

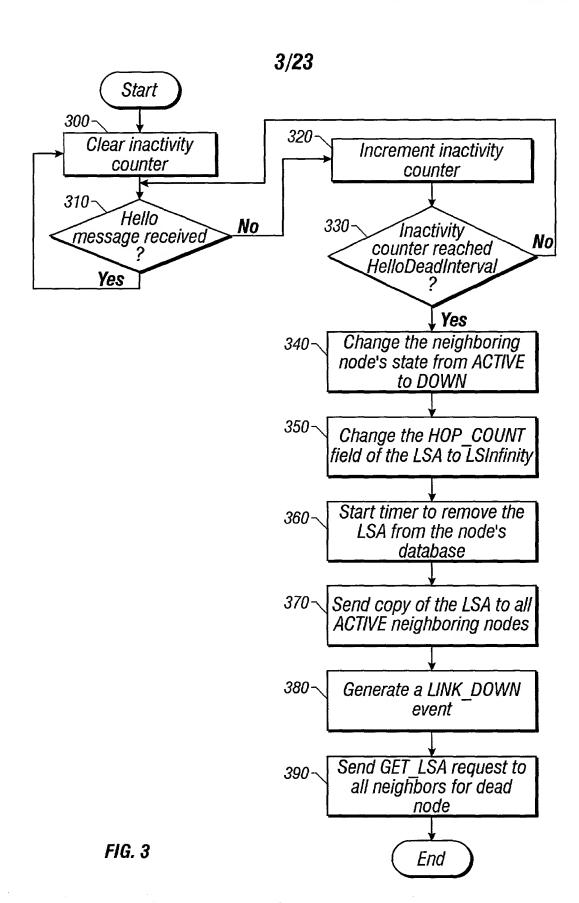
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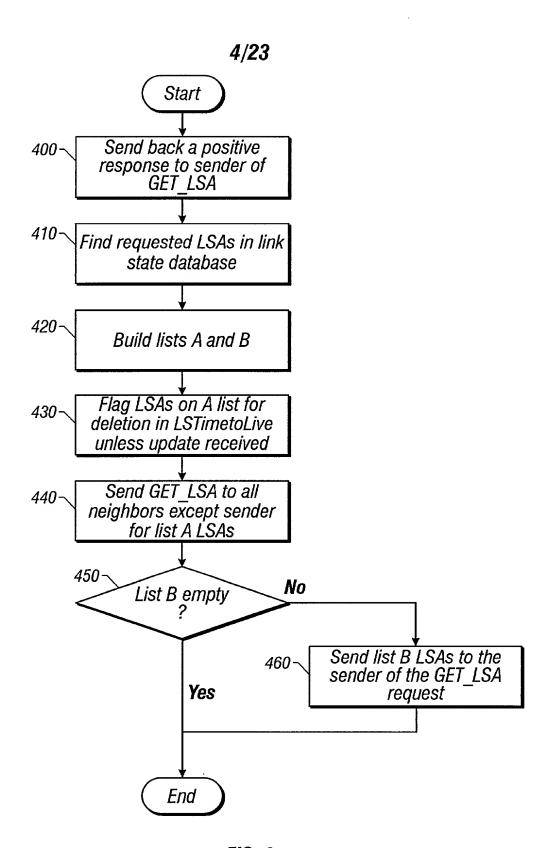
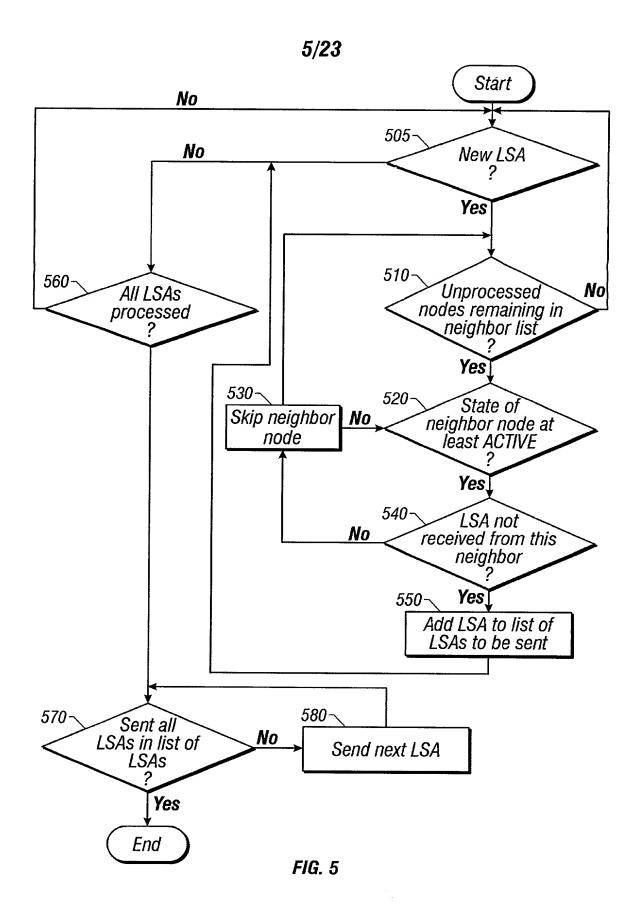
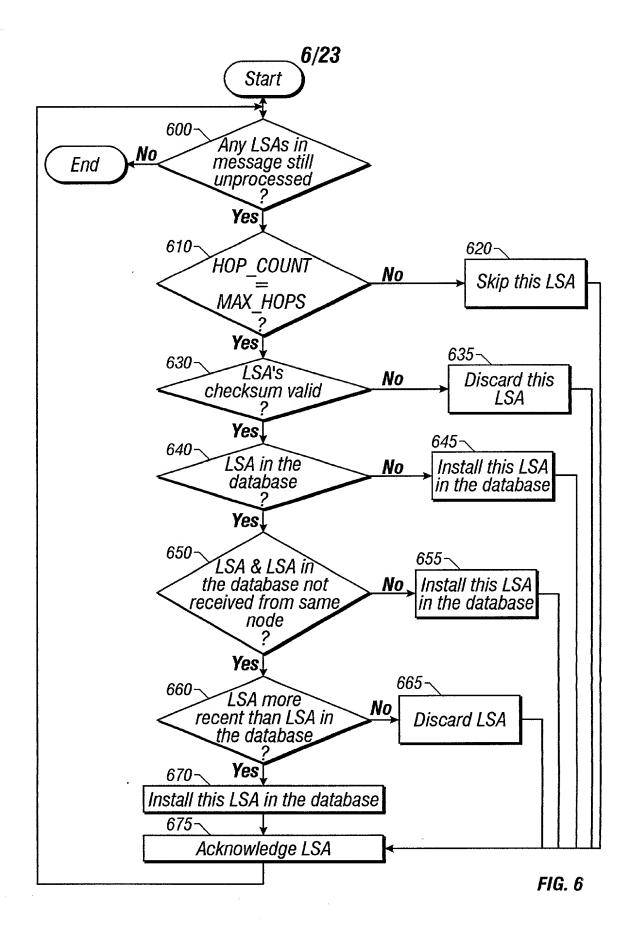


FIG. 4

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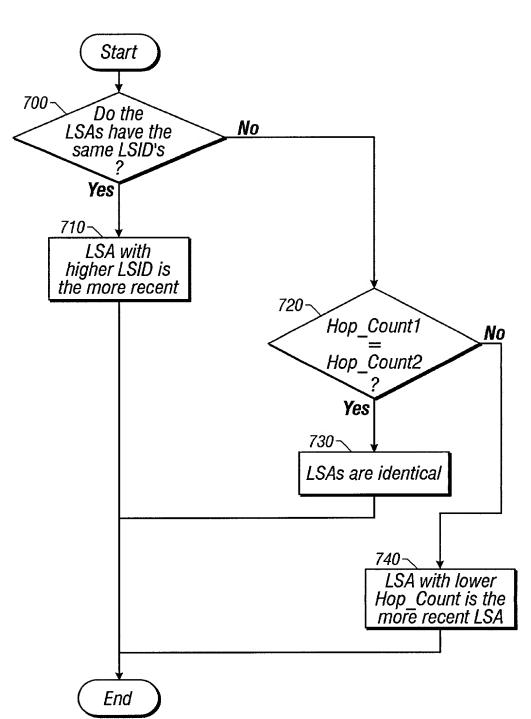


FIG. 7

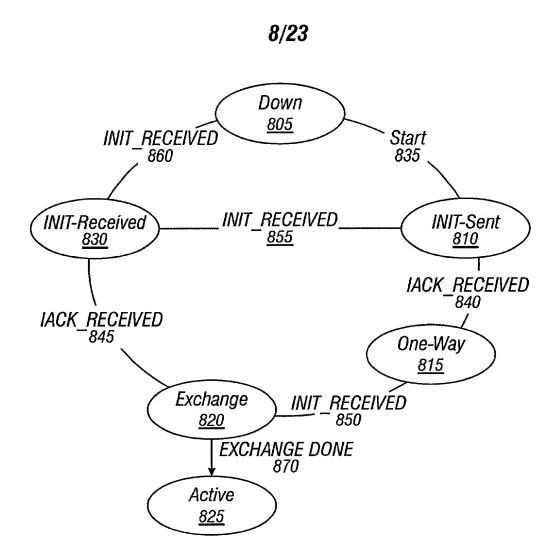


FIG. 8



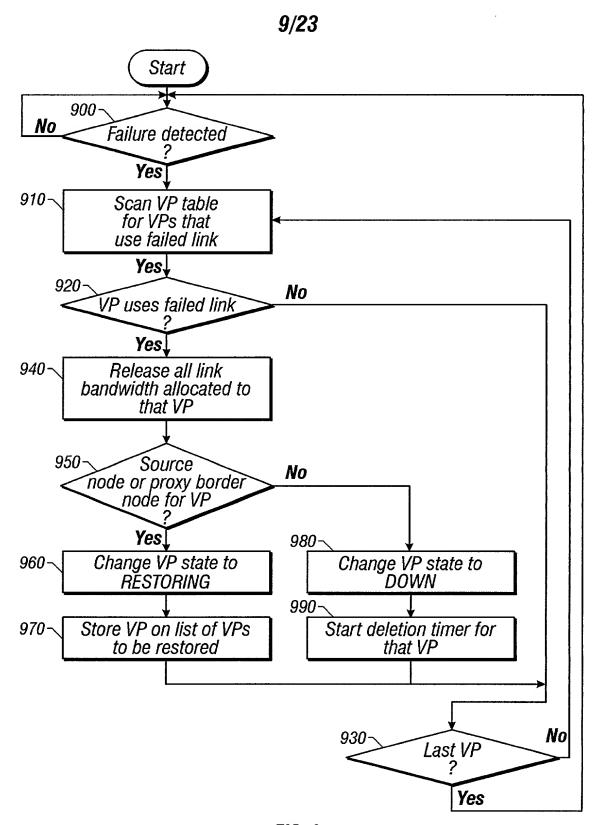
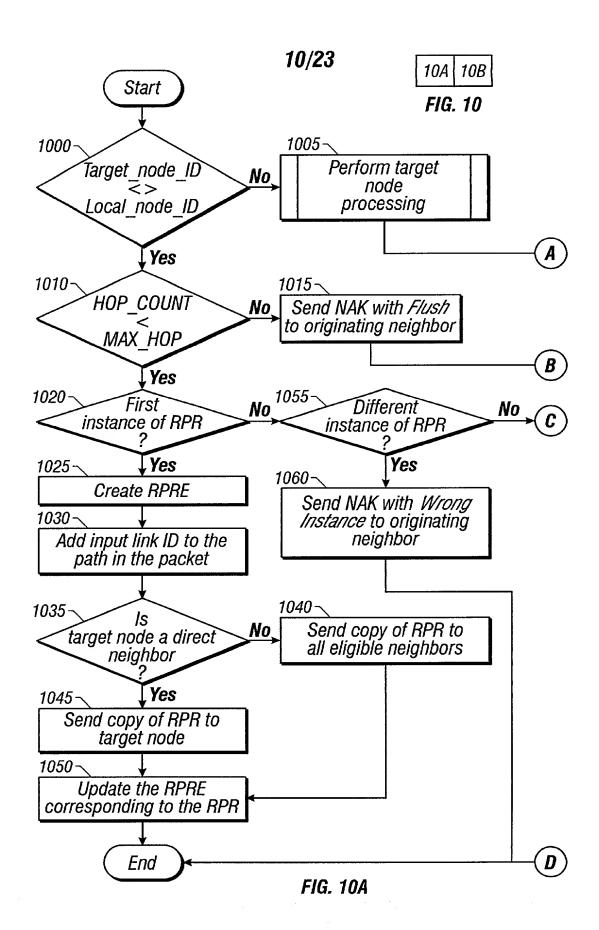


FIG. 9

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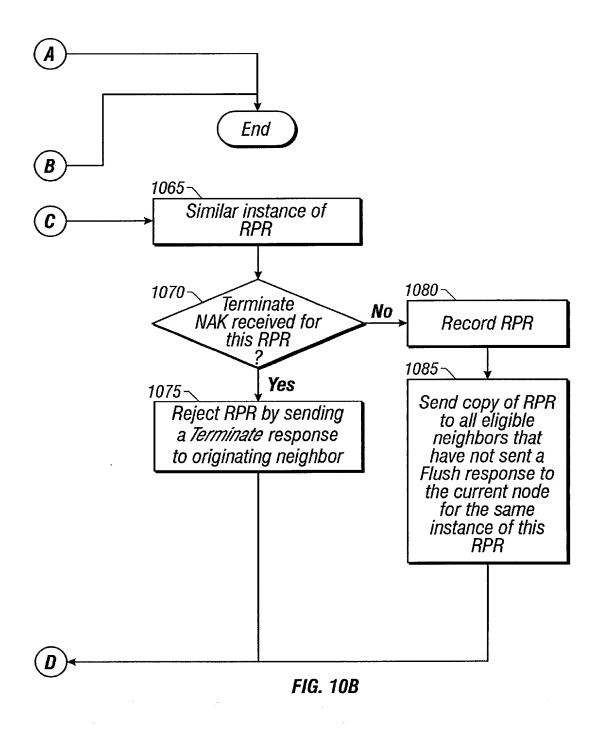


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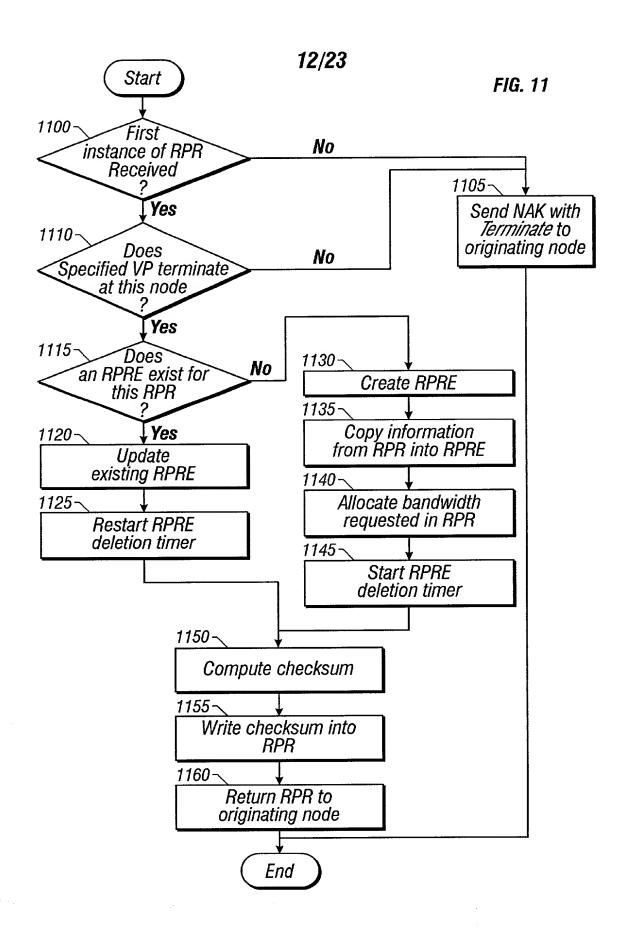
Title:

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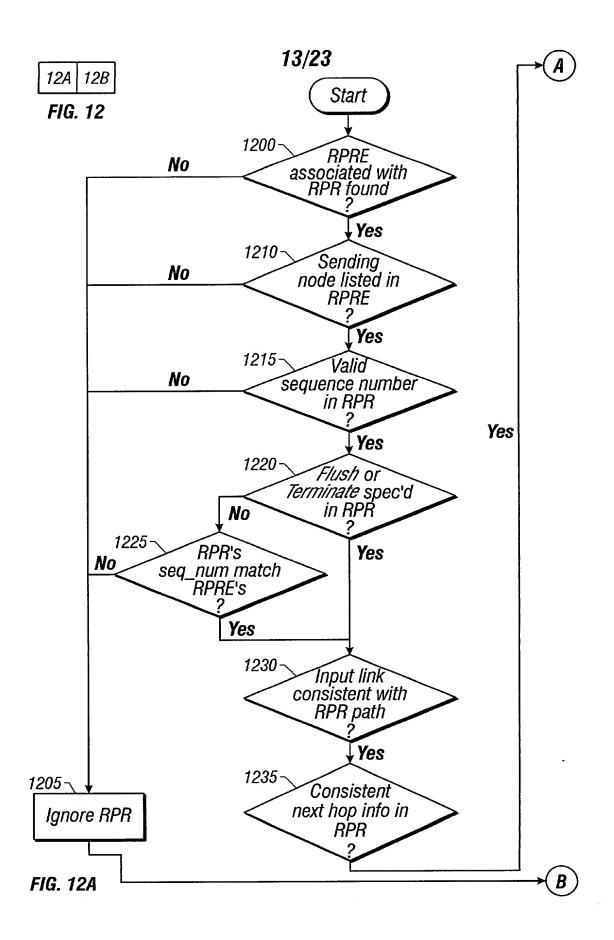


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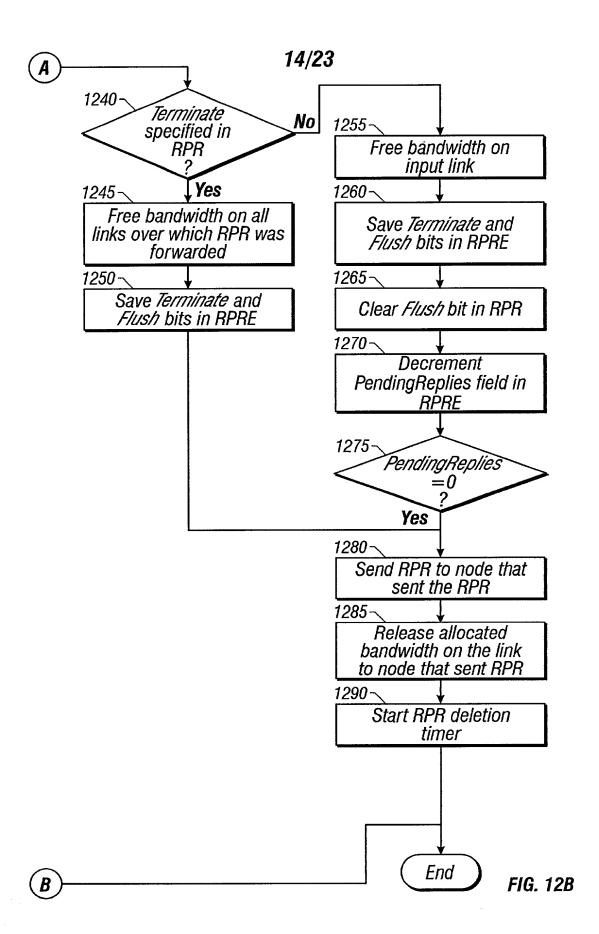
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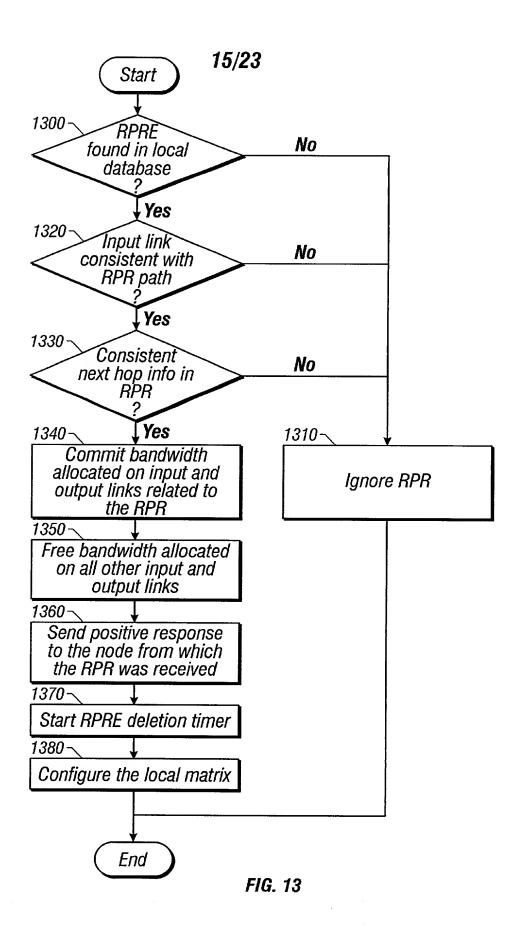


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Title:



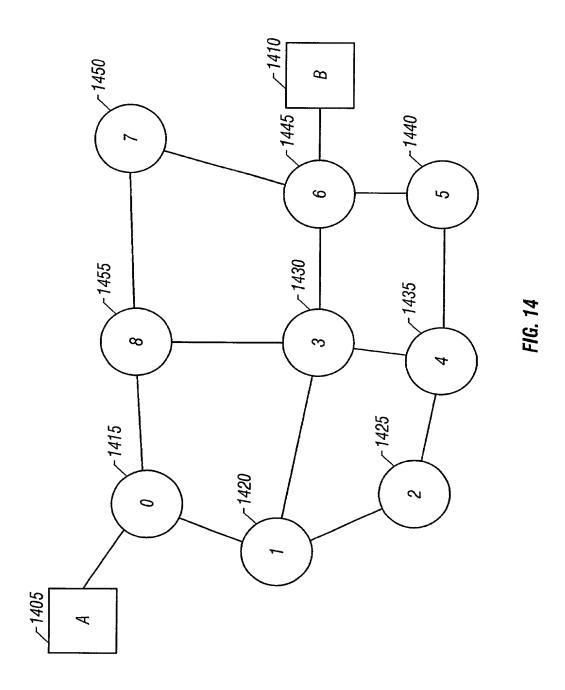
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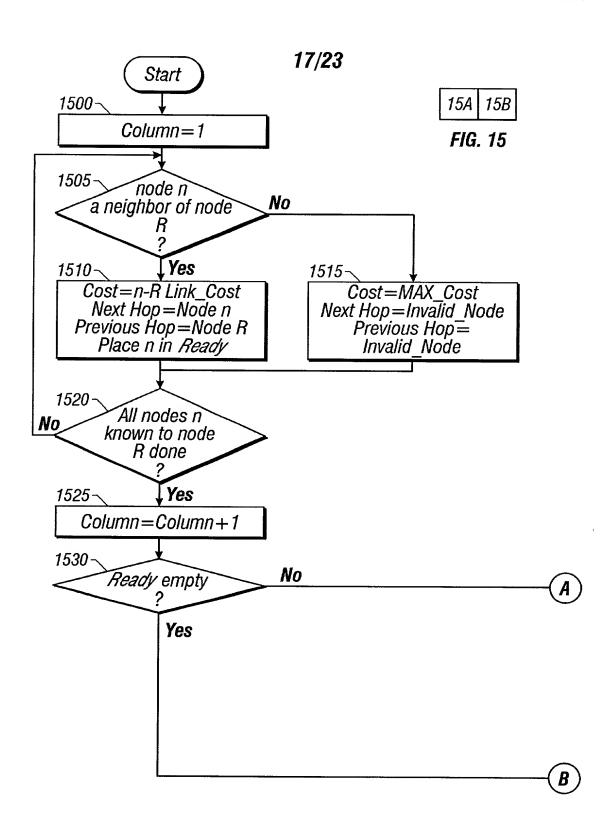
09/751,99 I Inventor: Ali N. Sal

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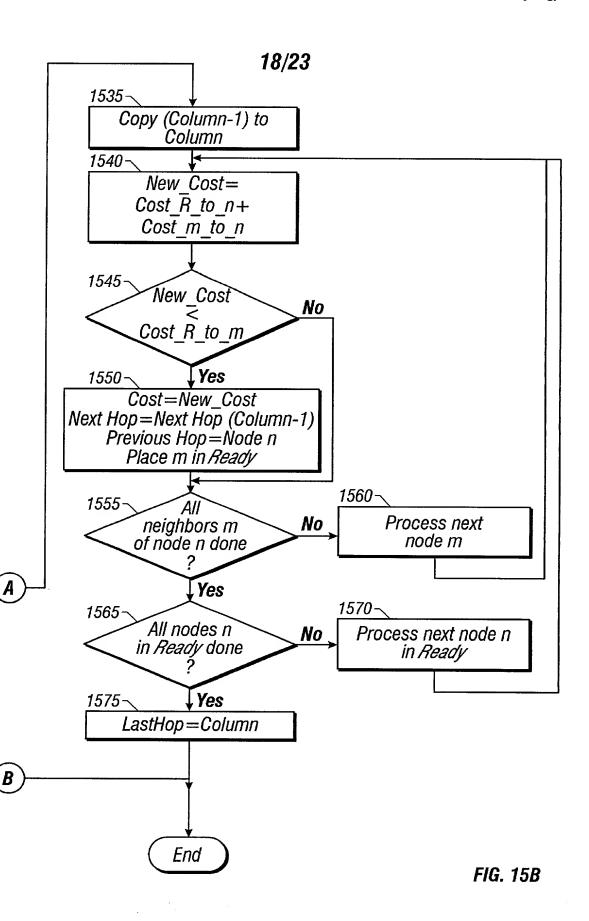


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Title:





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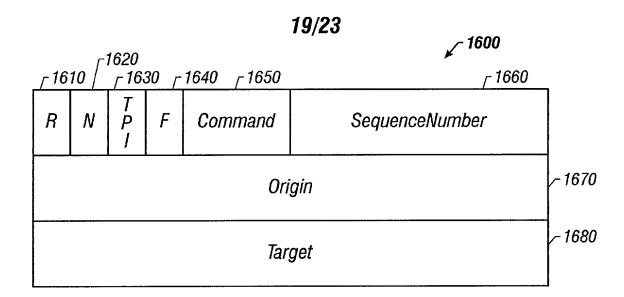


FIG. 16

√1700

NodelD	<u>1710</u>
LinkCost	<u>1720</u>
QoS3Capacity	<u>1730</u>
QoSnCapacity	<u>1740</u>
HelloInterval	<u>1750</u>
TimeoutInterval	<u>1760</u>

FIG. 17

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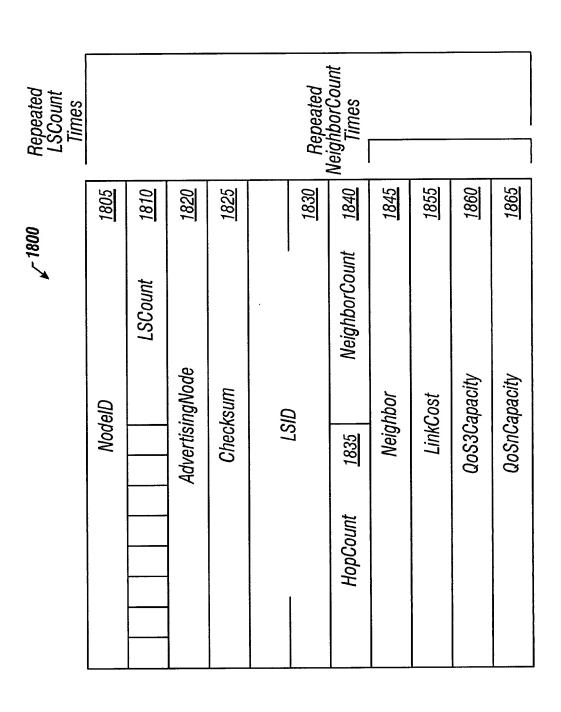


FIG. 18

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√ 1900

Zero-by	te <u>1905</u>	LSA count	<u>1910</u>	<u> </u>
	Nod	le ID	1930(1)	
	Nod	le ID	1930(2)	
	Nod	le ID	1930(N-1)	
	Nod	le ID	<u>1930(N)</u>	

FIG. 19

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∠ 2000

VPID		<u>2010</u>	
Checksum		<u>2020</u>	Path
<u>2030</u>	HopCount	<u>2040</u>	
Нор		<u>2060(1)</u>	
Нор		<u>2060(2)</u>	
		4	
Нор		2060(N)	
	Checks 2030 Hop	Checksum 2030 HopCount Hop	Checksum 2020 2030 HopCount 2040 Hop 2060(1) Hop 2060(2)

FIG. 20

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∠2100

VPID		<u>2110</u>		
	Checksum		<u>2121</u>	Path
PathLength	<u>2130</u>	HopCount	<u>2140</u>	
	Нор		<u>2160(1)</u>	
	Нор		2160(2)	
	Нор		2160(N)	

FIG. 21